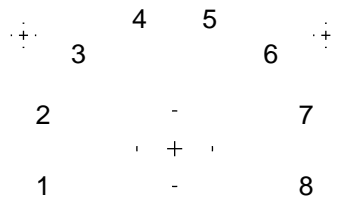
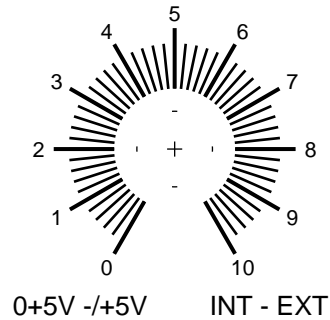


CV-RECORDER

MODE



CLOCK RATE



INPUT

GATE IN

-

-

+

+

-

-

OUT #1

OUT #2

-

-

+

+

-

-

TRIG #1

TRIG #2

-

-

+

+

-

-

ACSynth